

Investigation of the Input Unit of Frequency Converter

I. Rankis, A. Zhiravecka, I. Bunina
Riga Technical University (Riga, Latvia)
zhiravecka@eef.rtu.lv, rankis@eef.rtu.lv

Abstract – The paper considers scheme of single-phase input unit with AC circuit reactor of frequency converter, its operation, dependencies of characteristics on parameters of the scheme and influence on supply network.

I. INTRODUCTION

The modern input units of the frequency converter are formed according to either „diode rectifier - capacitor” or „diode rectifier – filter reactor - capacitor” principle [1, 2]. In the first case from the AC network the short pulses of high amplitude are consumed that determines a very poor form of the supply current with high factor of harmonics distortion THD [3]. However the capacitor voltage is close to the amplitude of the sine-form supply voltage that provides the necessary value of RMS voltage at the output of inverter.

The second variant improves the form of current and its THD is approximately 0,3-0,4, i.e., acceptable enough. Though as the average value of the voltage across the filter reactor is zero, the voltage of the capacitor is equal to that of diode rectifier output, that in the case of single phase bridge contains only 0,9 of the supply RMS voltage U_1 . For example, if $U_1=220V$, then the capacitor voltage is 198 V that is not enough to get necessary voltage, $3 \times 220V$, at the output of inverter in the realization without transformer.

As an alternative variant the input unit with reactor L in AC circuit before the bridge and capacitor in its output could be considered (Fig.1). In this realization the form of network current is satisfactory as well as capacitor voltage can achieve high enough values. The goal of this paper is to investigate key parameters and features of this scheme.

II. PROCESSES IN THE SCHEME

To simplify the description of the processes it could be accepted that capacitor C is of high enough capacity that in its turn provides an unchangeable voltage across its clamps in quasi-stationary process, i.e., $u_c=U_c$ and load current then is fully smoothed ($i_{sl} = I_{sl}$).

During each half-cycle of supply voltage a current through reactor L flows only from the moment t_s when

$$U_m \sin \omega t_s = U_c, \quad (1)$$

that leads to the current initial moment

$$t_s = \frac{1}{\omega} \arcsin \frac{U_c}{U_m}. \quad (2)$$

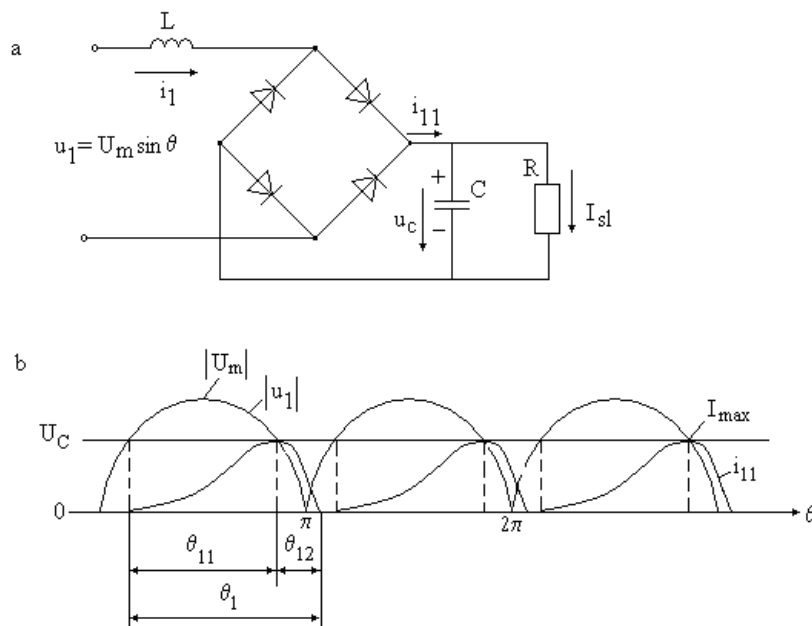


Fig.1. Scheme of the investigated single-phase AC input unit (a) and diagram of rectifier output current (b).

The reactor current reaches its maximum value at the time moment

$$t_m = \frac{\pi}{\omega} - \frac{1}{\omega} \arcsin \frac{U_C}{U_m}, \quad (3)$$

when from the following $U_m \sin \omega t_m = U_C$. Therefore the increasing of the current i_{11} from 0 to I_m lasts

$$t_{11} = t_m - t_s = \frac{1}{\omega} (\pi - 2 \arcsin \frac{U_C}{U_m}). \quad (4)$$

Current i_{11} during its increment time can be defined from the differential equation

$$L \frac{di_{11}}{dt} = U_m \sin \omega t - U_C, \quad (5)$$

where $t \geq t_s$. The solution of this equation is

$$i_{11} = \frac{U_m}{\omega L} + \frac{U_C}{L} (t_s - t), \quad (6)$$

where $i_{11} \geq 0$. At the end of the increment time

$$I_{\max} = \frac{2U_m}{\omega L} \cos \omega t_s - \frac{U_C}{L} t_{11}. \quad (7)$$

Then, as $u_1 < U_C$, current i_{11} starts its decreasing to zero during time interval t_{12} . It results in intermittent mode of the current i_{11} , so long as $U_C > 0,6U_m$.

However it is important to define the real value of U_C depending on load resistance R . The capacitor voltage increases in its value when $i_{11} > I_{sl}$ and decreases when $i_{11} < I_{sl}$. As the capacitor average current in steady-state is zero the average i_{11} is equal to I_{sl} . But the direct integration of expression (6) is complicated as the time interval t_1 of the current flow is difficult to be defined and calculated. Thus for the approximate calculations current i_{11} could be accepted as of triangle form with height I_{\max} and base t_1 and then

$$I_{\max} t_1 = \frac{U_C}{R} T, \quad (8)$$

where amplitude I_{\max} can be approximately calculated from

$$L \frac{I_{\max}}{t_{11}} = 0,7(U_m - U_C). \quad (9)$$

In order to apply these simplified expressions it is necessary to define the coherence of interval t_1 with parameters L and R with the help of computer model. The obtained dependences of $t_1 = f(R)$ at different L (Fig.2) within the considered parameter range $R \leq 10 \Omega$, $1 \leq L \leq 5 \text{ mH}$, $U_m = 312 \text{ V}$, $f = 50 \text{ Hz}$, $C = 20000 \mu\text{F}$ can be considered as close to linear.

This implies the calculation of interval t_1 with a low error as

$$t_1 = (7,67 + 1,0175L^* - 1,4225R^*) \cdot 10^{-3} \text{ s}. \quad (10)$$

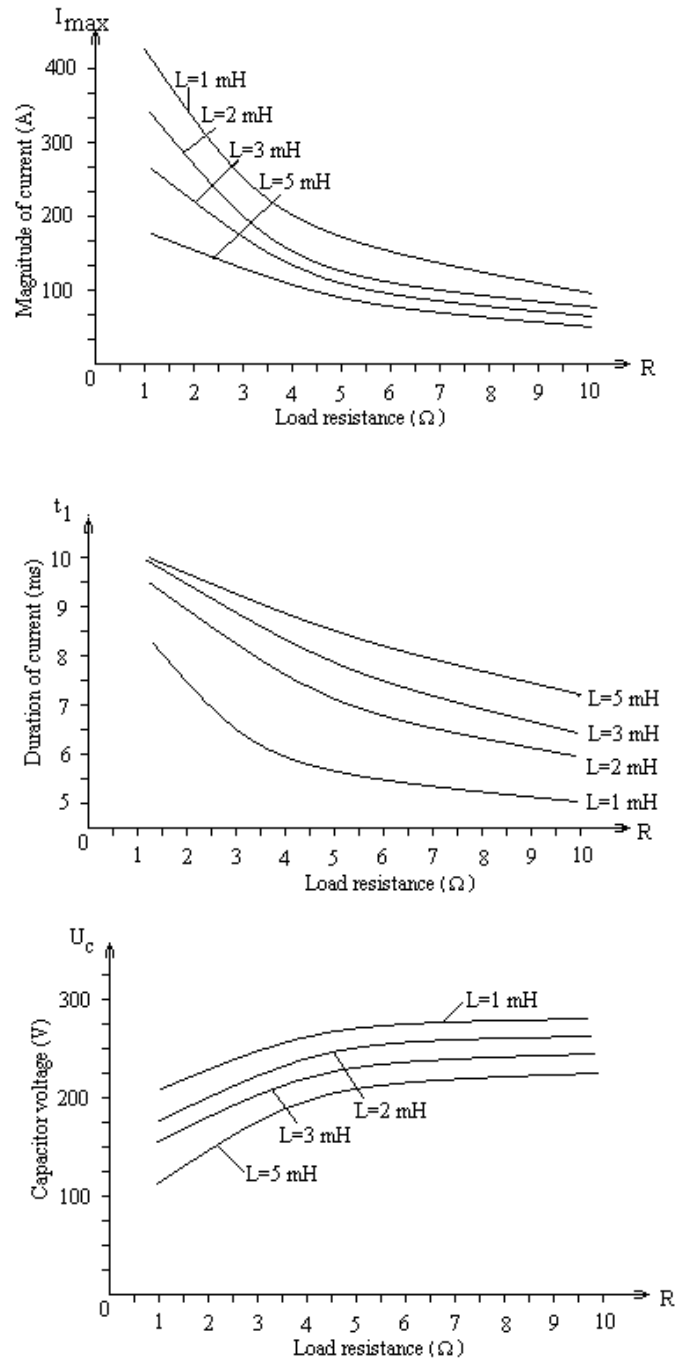


Fig.2. Characteristics from computer modeling of the scheme: magnitude of current $I_{\max} = f(R)$, duration of current pulse $t_1 = f(R)$ and voltage of capacitor $U_C = f(R)$ at different values of reactor's inductance L within the determined parameter range

Important is restriction that $t_1 \leq 10 \text{ ms}$. Here the normalized parameters L^* and R^* are within the range from -1 (the lowest value) to +1 (the highest value) and determined as

$$X^* = \frac{2X - X_{\max} - X_{\min}}{X_{\max} - X_{\min}} \quad (11)$$

From the computer model we can also conclude that time demonstrates that with the decreasing of values of the load resistance and inductivity of the reactor the amplitude of the current increases, but the average value of the capacitor voltage decreases with the decreasing of the load resistance and inductivity increasing.

Applying the above given statistical expression the voltage of the capacitor is defined as

$$U_C = \frac{0.455(7.67 + 1.0175L^* - 1.4225R^*)^2}{\frac{LT \cdot 10^6}{R} + 0.455(7.67 + 1.0175L^* - 1.4225R^*)^2} U_m \quad (12)$$

and with the same operation,

$$I_{\max} = \frac{U_m T \cdot 0.455(7.67 + 1.0175L^* - 1.4225R^*)}{R \cdot 10^3 \left[\frac{LT}{R} + \frac{0.455}{10^6} (7.67 + 1.0175L^* - 1.4225R^*)^2 \right]} \quad (13)$$

The obtained equations allow calculation t_1 , U_C and I_m with a comparatively low error (Table I).

TABLE I
ESTIMATION OF THE ERROR OF CALCULATIONS

Parameters	L*	R*	Computer model	Calculation
t_1	-1	-1	8,2 ms	8,075 ms
	-1	+1	5,12 ms	5,23 ms
	+1	+1	7,39 ms	7,26 ms
	+1	-1	10 ms	10 ms
U_C	-1	-1	203,7 V	186,37 V
	-1	+1	270,5 V	268,9 V
	+1	+1	226,8 V	220,2 V
	+1	-1	107 V	99 V
I_m	-1	-1	426,8 A	461,57 A
	-1	+1	95,4 A	102,8 A
	+1	+1	55,1 A	60,66 A
	+1	-1	173,6 A	195,7 A

III. POWER INDICES OF THE SCHEME

Assuming the form of the network current during the half-period as triangle pulse the RMS current is calculated as

$$I_{1ef} = I_m \sqrt{\frac{2t_1}{3T}} \quad (14)$$

and the total power consumed from the network is

$$S = U_1 I_{1ef} = U_1 I_m \sqrt{\frac{2t_1}{3T}} \quad (15)$$

The active power is

$$P = \frac{U_C^2}{R} \quad (16)$$

Calculating power factor as a ratio of these two values taking into account the obtained before expressions (8), (12), (13)

$$\chi = \frac{P}{S} = \frac{I_{\max} t_1^2 R}{U_1 T^2 \sqrt{\frac{2t_1}{3T}}} = \frac{0.455 \cdot t_1^3}{T \cdot \left[\frac{LT}{R} + 0.455 t_1^2 \right] \sqrt{\frac{t_1}{3T}}} \quad (17)$$

With the help of this equation P/S ratio dependence on R and L changes can be calculated (Table II).

TABLE II
ESTIMATION OF THE POWER FACTOR INDEX

Parameter	L*	R*	Computer model	Calculation
I_{1ef} , A	-1	-1	257	239,46
	-1	+1	44,07	42,92
	+1	+1	31,07	29,84
	+1	-1	120,3	113
S, VA	-1	-1	56540	52683
	-1	+1	9695	9443
	+1	+1	6835	6565
	+1	-1	26466	24857
P/S	-1	-1	0,728	0,66
	-1	+1	0,752	0,765
	+1	+1	0,75	0,738
	+1	-1	0,438	0,39

It is obvious that computer model as well as approximate calculations provide close enough results allowing to conclude that with low load resistance power indices increase with the decreasing of input reactor inductivity. Fig.3. represents dependences of $\chi=f(R)$ at different values of L obtained in the computer simulation and calculations as well for comparison. From the graph it is seen that the unit provides the highest power parameters within the middle of the load zone.

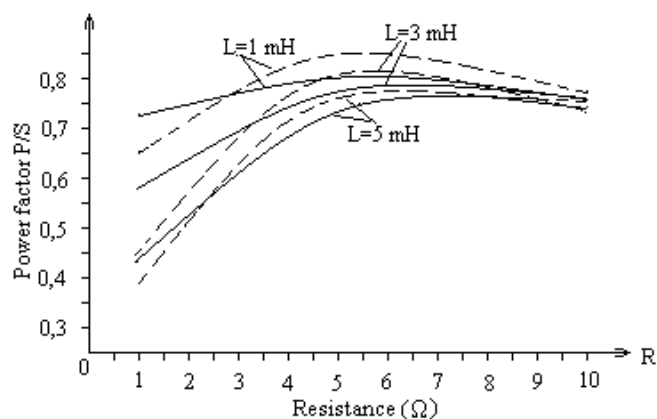


Fig.3. Dependences of $\chi=f(R)$ at different L obtained in the computer simulation (uninterrupted line) and in the calculations (dashed line)

The second power parameter is the index of total harmonic distortions of the network current. Thus for the calculations the RMS value of current $I_{(1)}$ must be calculated. Assuming the form of the network current during the half-period as

triangle the RMS value of the basic harmonic is approximately determined as

$$I_{1(t)} \approx I_1 \sqrt{\frac{2t_1}{T}} \quad (18)$$

and as a result THD is

$$THD \approx \sqrt{\left(\frac{T}{2t_1}\right)^{\frac{2}{3}} - 1} \quad (19)$$

Depending on R^* and L^* values in the investigation range the following estimation expression can be obtained with the use of computer modeling

$$THD = 0.346 + 0.173R^* - 0.14L^* \quad (20)$$

Figure 4 demonstrates graphs $THD=f(R)$ obtained in the computer modeling and with the help of estimating expression at different L . The estimating expression gives the results close enough to those experimentally obtained.

IV. SELECTION OF THE PARAMETERS

Inductivity of reactor L can be defined according to a necessary U_C/U_m ratio with the different given load resistances. Expression (12) could be applied for this aim taking into account that this satisfactory relation is provided within the zone $-1 \leq L^* \leq +1$. Still with sufficiently good results that could be applied also with $L^* \geq -1.3$. Thus, for example, if $R^* = -0.8$ and the ratio of capacitor voltage is $U_C/U_m = 0.85$, in accordance with equation (12), if the voltage of capacitor is smoothed, a reactor with inductivity $L^* = -1.3$ (0.4 mH within the assumed data range) can be used. From the computer model the same voltage ratio $R^* = -0.8$ can be obtained with $L^* = -1.35$ (i.e., $L = 0.3$ mH).

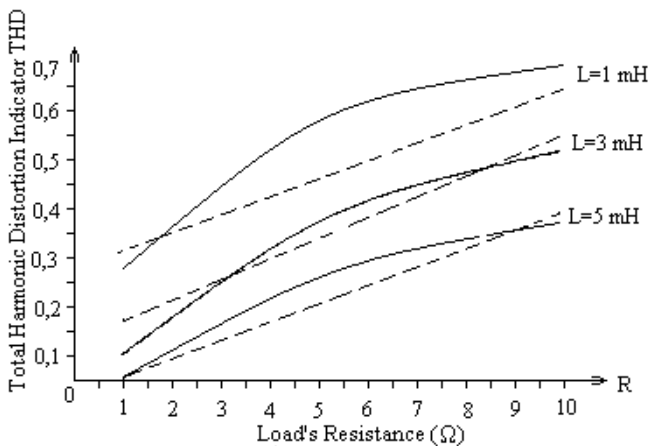


Fig.4. Characteristics $THD=f(R)$ at different L in computer modelling (uninterrupted line) and in calculation (dashed line)

The selection of a necessary capacitance can be realized in accordance with the approximate expression

$$\Delta U_C = \frac{U_C \left(\frac{T}{4} - \frac{t_1^2}{2T} \right)}{RC} \quad (21)$$

where ΔU_C is a full increment of an instantaneous value of the capacitor C voltage during a half-period (fig.5). The expression is approximate assuming the form of the output current of the rectifier as triangle (Fig.5) and applying the equation (8). For example, assuming $U_C/U_m = 0.1$ with $R^* = -1$ and $L^* = -1$ the necessary capacity is 33700 μF .

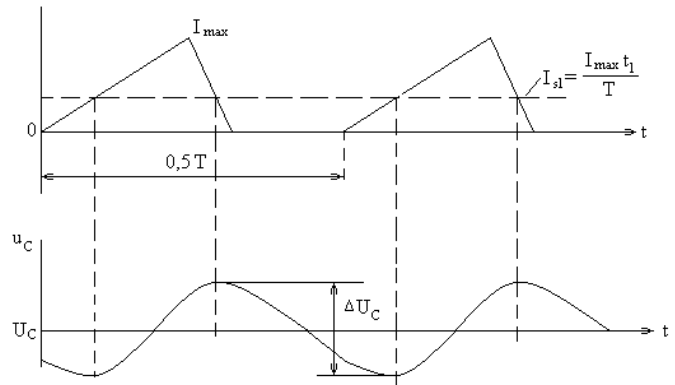


Fig.5. Diagram of voltage ripple of the capacitor C voltage during a half-period

VI. CONCLUSIONS

The considered topology of the input unit provides enough high voltage of the output capacitor of the rectifier as well as satisfactory form of the current of single-phase AC network.

The precise calculations according to differential equations in this scheme are complicated therefore a simplified approach could be applied on the base of linearization of the network current form and statistical estimation of the dependences of the duration of the current impulse within half-period on the inductivity of the reactor and load resistance, that can be considered close to linear relations.

To obtain a higher output voltage at lower load resistances the decreasing of the amplitude of reactor current is required, that decreases the duration of the current impulse and worsens the power factor and harmonic distortion.

In the considered range of L and R parameters with the network voltage 220V, 50 Hz the mathematical expectation of the voltage is 221.75V, the duration of the impulse of network current is 7.65ms, the amplitude of current impulse is 102.8A, power parameters – $P = 8941W$, $S = 11435VA$, $P/S = 0.78$ and THD of the network current is 0.346.

REFERENCES

- [1] Power Electronics. Handbook/ Muhammad H.Rashid, Editor in chief. Academic Press: NY,2001, 895 pp.
- [2] N. Mohan, T.Undeland, W. Robbins Power Electronics: Converters, Applications and Design, NY, 1989, 667 pp.
- [3] D.A.Jarc, R.G. Schieman, Power Line consideration for Variable Frequency Drives, IEEE Trans.on Ind.Applic. Vol.IAS,No5 1985,pp.1099-1105.